

About Valingro Group

- Valingro Group facilitates, develops & build Enterprises girded by Values
- Creating Companies concentrating on Global Competitiveness, leveraging global Opportunities, Acquiring global capabilities
- Philosophy: Business Beyond Profit
- Long-Term Value-Added Partnership Business Model

About ChipTest

- An associate of Valingro, ChipTest is an IC Test Company
- Established in 2005, ChipTest has 2 Core BUs: Test Engineering and Wisteria
- Over 30+ Years of Test Engineering Business Experience
- Operations in India, Singapore & Malaysia

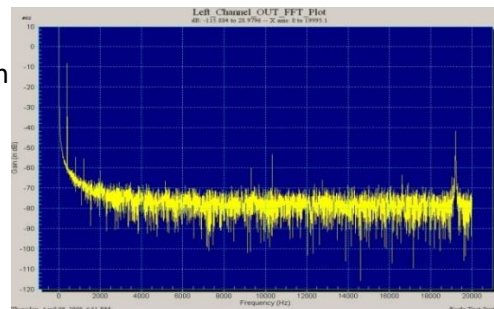
Services

- Test Plan Derivation for New Silicon Products
- Turnkey Hardware & Software development
- ATE Test Program Generation & Debug
- Product Characterization & Correlation
- Program Conversions between platforms
- Engineering to Production Integration
- Onsite Test Engineering Support
- Offshore Customer centric Test Centres
- Bench Validation of NPI products
- Vector Conversion - Design Files to ATE Format
- Customers – IDM, Fabless, Start-up, Subcon
- USP – Dedicated Teams, Fast Time to Market



Process Flow

- Product Design documents / Datasheet Review
- Test Methodology & Limits Tabulation
- Test Hardware Schematics, Layout and Gerber Generation
- Test Hardware Fabrication and Assembly
- ATE Test Program Code Development
- Multi-site Test Program & Hardware Debug
- Test Data Correlation & Statistical Analysis
- Electrical & Temperature Characterization
- Qualifications – FMEA, Site to Site, Spike Checks, etc
- GRR Verification & Capability Studies
- Test Time Optimization
- Manufacturing Integration & Qualification



Capabilities

- Advantest 93K ATE
- Advantest T2000 IPSE / IPS and T65XX ATE
- Teradyne ETS 800, ETS 364, ETS 88, ETS 500, J750, IFLEX ATE
- ASL1K ATE
- Gravity Feed, Pick & Place, Turret & Map Handlers and Probers
- Bench Power Supplies, Oscilloscopes, V/I Meters, Thermo stream & Others
- Offline ATE Simulators and Remote Login Access
- Start-up Customers – Turnkey from Post Silicon to Manufacturing
- IDM Customers – Development to Manufacturing Release



Road Map

- High End Mixed Signal Tester
- RF Tester



Expertise

- Automotive products, High speed Digital SoC, Logic devices, Clock Drivers, Gate Drivers, Power Management
- BMS, Mixed Signal ASIC, A/D & D/A Converters, Audio processors, etc
- High Voltage / High Current & Low Voltage / Low Current Handling
- High Speed AWG / Digitizer Handling with DSP Commands & Data Analysis
- ATPG / Functional / Scan Vectors Handling & Scripting Skills
- Multi-site Cable Dock & Direct Dock Production Solutions
- Over 100 years of cumulative experienced Test Engineers
- Case Studies – A/V Decoders, DSP Controllers, Micro Controllers, etc
- Case Studies – DDR Power Controllers, Multiphase Controllers, Battery Chargers, LED Charge Pump Drivers, etc

